

CLAIM AMENDMENTS

Claims 1-12 (Cancelled)

Sub C1
13. (Currently Amended) A display comprising:
a memory array;
a pixel array; and
a refresh circuit coupled to said memory array and said pixel array, said refresh circuit adapted to feed data received from said memory array back to said memory array to refresh said memory array, and said refresh circuit adapted to refresh said pixel array.

14. (Original) The display of claim 13 wherein said memory array and said pixel array are formed in the same semiconductor substrate with said refresh circuit.

B1
15. (Original) The display of claim 14 wherein said substrate is a liquid crystal over semiconductor substrate, said pixel array including a plurality of electrodes adapted to interact with a liquid crystal material over said pixel array.

16. (Original) The display of claim 13 wherein said memory array is formed of dynamic random access memory.

Claims 17-25 (Cancelled)

26. (New) A display comprising:
a memory array;
a pixel array; and
a refresh circuit coupled to said memory array and said pixel array, said refresh circuit adapted to refresh said memory array and said pixel array, and said memory array and said pixel array are formed in the same semiconductor substrate with said refresh circuit.

27. (New) The display of claim 26 wherein said substrate is a liquid crystal over semiconductor substrate, said pixel array including a plurality of electrodes adapted to interact with a liquid crystal material over said pixel array.

28. (New) The display of claim 26 wherein the pixel array is coupled to the memory array by a digital to analog converter.

29. (New) The display of claim 26 wherein the pixel array forms a reflective liquid crystal spatial light modulator.

30. (New) The display of claim 26 wherein the pixel array is adapted to eliminate a need for a periodic pixel refresh cycle.

31. (New) A display comprising:
a memory array formed of dynamic random access memory;
a pixel array; and
a refresh circuit coupled to said memory array and said pixel array, said refresh circuit adapted to refresh said memory array and said pixel array.

32. (New) The display of claim 31 wherein the pixel array is coupled to the memory array by a digital to analog converter.

33. (New) The display of claim 31 wherein the pixel array forms a reflective liquid crystal spatial light modulator.

34. (New) The display of claim 31 wherein the pixel array is adapted to eliminate a need for a periodic pixel refresh cycle.